

Web Images Maps News Shopping Gmail more ▾

drjatorres@gmail.com | My Notebooks | Web

History | My Account | Sign out



"first counter" "second counter" "sample rate converter"

Search

Advance
Preferences

Web Books

Results 1 - 10 of about 146 for "first counter" "second counter" "sample rate converter". (0.33 seconds)

Sample rate converter for digital video signals having reduced ...

5182633, Video sample rate converter, January, 1993, Antonio et al. ... said means for forming the second phase signal includes a second counter which ...
www.freepatentsonline.com/5285263.html - Similar pages - Note this
 by T Fujita - 1994 - Cited by 1 - Related articles - All 2 versions

Asynchronous sample rate converter and method - Patent 7262716

The asynchronous sample rate converter of claim 4 wherein the address generation circuitry includes: a second counter clocked by the input sample rate clock ...
www.freepatentsonline.com/7262716.html - Similar pages - Note this
 by X Yu - 2007 - Related articles - All 2 versions
 More results from www.freepatentsonline.com »

Method and system for sampling rate conversion in digital audio ...

a third variable to store the first counter, a fourth variable to store a second counter to track the samples of the first digital audio signal, and ...
www.patentstorm.us/patents/6508850-claims.html - Similar pages - Note this

Asynchronous sample rate converter and method - US Patent 7262716

An asynchronous sample rate converter interpolates and filters a digital ... a first adder (69) combining the digital output of the second counter (65) with ...
www.patentstorm.us/patents/7262716-description.html - Similar pages - Note this
 More results from www.patentstorm.us »

Inventors Thomas C. Savell, Santa Cruz CA David Rossum, Monterey ...

The apparatus of claim 1 wherein the first counter, second counter or third counter is an ... sample rate of an asynchronous digital sample rate converter. ...
www.patentlens.net/patentlens/search_ajax.cgi?patnum=US+6324235 - 26k -
 Cached - Similar pages - Note this

Inventors Seiichi Mizukoshi, Kanagawa (JP) Tomonori Satoh, Tokyo ...

b. a first counter supplied with said synchronization signal for frame scanning and counting the same; c. a second counter supplied with said clock signals ...
www.patentlens.net/patentlens/search_ajax.cgi?patnum=US+4891713 - 18k -
 Cached - Similar pages - Note this
 More results from www.patentlens.net »

Combining Or Distributing Information Via Time Channels ...

A receiver includes a sample rate converter configured to receive an input links includes a first counter that generates a first local network clock, ...
www.freshpatents.com/x1370503000psbc.php - 131k - Cached - Similar pages - Note this

Multiplex communications inventions April - patents by class relation

Freshpatents.com offers information on a variety of new patent applications, updated each week - check out Multiplex communications inventions April ...
www.freshpatents.com/Multiplex-communications-dt200704ntc370.php - 412k -
 Cached - Similar pages - Note this

Message routing through data communication networks

Message routing through data communication networks. 4645874. Abstract. A mechanized system distributing the access, test and communication functions to the ...
www.palmerpatent.com/.J
[4645874_message_routing_through_communication_networks.html](#) - 443k -
Cached - Similar pages - Note this

Asynchronous digital sample rate converter - Google Patents Result

US Pat. 5475626 - Analog Devices, inc.

Asynchronous digital sample rate converter ... and a terminal count output which indicates overflow of the output of the first counter; a second counter ...

www.google.com/patents?id=WjYcAAAAEBAJ

1 2 3 4 Next

"first counter" "second counter" "sample rate converter"

Search

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#) |
[Try Google Experimental](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [Privacy](#) - [About Google](#)

Web Images Maps News Shopping Gmail more ▾

drjatorres@gmail.com | My Notebooks | Web

History | My Account | Sign out



"first counter" "second counter" "sample rate converter" "digital data stream"

Search

Web

Results 1 - 6 of 6 for "first counter" "second counter" "sample rate converter" "digital data stream". (0.26

Asynchronous sample rate tracker - Patent 6324235

The apparatus of claim 1 wherein the first counter, second counter or third ... A

synchronous sample rate converter shares a common time base, or clock, ...

www.freepatentsonline.com/6324235.html - Similar pages - Note this

by TC Savelli - 2001 - Cited by 4 - Related articles - All 3 versions

Asynchronous sample rate estimation using reciprocal frequency ...

The apparatus of claim 1, further comprising a sample rate converter circuit ... wherein the first counter and the second counter is an overflow buffer. ...

www.freepatentsonline.com/6819732.html - Similar pages - Note this

by TC Savelli - 2004 - Related articles - All 4 versions

More results from www.freepatentsonline.com »

Asynchronous sample rate estimation using reciprocal frequency ...

A synchronous sample rate converter shares a common time base, or clock, which convert a first digital data stream to another digital data stream, ...

www.patentstorm.us/patents/6819732-description.html - Similar pages - Note this

Systems and methods for clock mode determination utilizing ...

4A, a first counter 401a counts periods of the MCK signal and a second counter 401b counts periods of the FIXCLK signal. The resulting count values ...

www.patentstorm.us/patents/7236109-description.html - Similar pages - Note this

More results from www.patentstorm.us »

Asynchronous sample rate tracker - Patent Review 6324235

The apparatus of claim 1 wherein the first counter, second counter or third ... An

asynchronous sample rate converter does not require a common time base ...

www.wikipatents.com/6324235.html - 147k - Cached - Similar pages - Note this

Asynchronous sample rate estimation using reciprocal frequency ...

The apparatus of claim 9, wherein the first counter and the second counter is an overflow buffer. 14. The apparatus of claim 1, wherein the input sample ...

www.wikipatents.com/6819732.html - 206k - Cached - Similar pages - Note this

More results from www.wikipatents.com »

In order to show you the most relevant results, we have omitted some entries very similar to the 6 already displayed.

If you like, you can repeat the search with the omitted results included.

"first counter" "second counter" "sample rate converter" "digital data stream"

Search

Search within results | Language Tools | Search Tips | Dissatisfied? Help us improve |

Try Google Experimental

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [Privacy](#) - [About Google](#)


[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) | [Purchase History](#)

Welcome United States Patent and Trademark Office

[Search Results](#)
[BROWSE](#)
[SEARCH](#)
[IEEE XPLORE GUIDE](#)

Results for "((first counter and second counter and sample rate converter and digital data stream)-in>metad..."

Your search matched 0 of 1865909 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.


[Search Options](#)
[View Session History](#)
[New Search](#)

Key

IEEE JNL	IEEE Journal or Magazine
IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

☐ Check to search only within this results set

 Display Format: ☒ Citation ☐ Citation & Abstract

[IEEE/ET](#)
[Books](#)
[Educational Courses](#)
[A](#)

IEEE/ET journals, transactions, letters, magazines, conference proceedings, and

[Select All](#)
[Deselect All](#)

No results were found.

Please edit your search criteria and try again. Refer to the Help pages if you need assistance.

[Help](#) [Contact Us](#)

Copyright 2008





Home | Login | Logout | Access Information | Alerts | Purchase History |
 Welcome United States Patent and Trademark Office

Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

Results for "((counter and sample rate converter)-in>metadata)"

Your search matched 1 of 1865909 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.



» Search Options

View Session History

New Search

Key

IEEE JNL IEEE Journal or Magazine
 IET JNL IET Journal or Magazine
 IEEE CNF IEEE Conference Proceeding
 IET CNF IET Conference Proceeding
 IEEE STD IEEE Standard

Modify Search

((counter and sample rate converter)-in>metadata)

Search

Check to search only within this results set

Display Format: Citation Citation & Abstract

IEEE/ET

Books

Educational Courses

A

IEEE/ET journals, transactions, letters, magazines, conference proceedings, and

view selected items

Select All

Deselect All

1. Synchronization of fractional interval counter in non-integer ratio sample
 Ketola, J.; Vankka, J.; Halonen, K.;
 Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International
 Volume 2, 25-28 May 2003 Page(s):II-89 - II-92 vol.2
 AbstractPlus | Full Text: PDF(332 KB) IEEE CNF
 Rights and Permissions

Help Contact Us

Powered by
 Inspec®

© Copyright 2008

1-9 of 9 hits for "first counter" "second counter" "sample rate converter" "digital data stream"

☐ Email, Save or Export checked results

Sor

Filter search results by
Content sources
<input type="checkbox"/> Journal sources
<input checked="" type="checkbox"/> Preferred web (9)
▪ Patent Offices (9)
<input type="checkbox"/> Other web
File types
▪ HTML (9)

Refine your search
<ul style="list-style-type: none"> ▪ ratio measurement ▪ data converter ▪ signal generation ▪ operating mode ▪ clock signal ▪ frequency measurement ▪ clock frequency ▪ frequency ratio ▪ mode selection ▪ internal clock
more >

- ☐ 1. [Asynchronous sample rate tracker](#)
Savell, Thomas C. / Rossum, David (Creative Technology, Lt
PATENT AND TRADEMARK OFFICE GRANTED PATENT, Nov 2001
patno:US6324235
...I. Overview of the Sample Rate Converter The present inventio
sample rate converter. An asynchronous sample rate converte
digital...that convert a first digital data stream to another digita
Full text available at patent office. For more in-depth search
[similar results](#)
- ☐ 2. [Asynchronous sample rate estimation using reciprocal frequency](#)
Savell, Thomas C. (Creative Technology Ltd.), *UNITED STATE
OFFICE GRANTED PATENT*, Nov 2004
patno:US6819732
...operating as an asynchronous sample rate converter, or is use
convert a first digital data stream to another digital data stream
asynchronous sample rate converter according to an embodime
Full text available at patent office. For more in-depth search
[similar results](#)
- ☐ 3. [VOICE GATEWAY WITH DOWNSTREAM VOICE SYNCHRONIZATION](#)
RABENKO, Theodore F. / HARTMAN, David / THI, James, C.,
(Corporation), *EUROPEAN PATENT*, Sep 2002
patno:EP1238489
...patcit> discloses the use of a sample rate converter (SRC) rec
stream at a first sample rate, outputting the...sample rate. The sa
rate converter is controlled depending on the fill...
Full text available at patent office. For more in-depth search
[similar results](#)
- ☐ 4. [Systems and methods for clock mode determination utilizing hyste](#)
Dewuer, Bruce Eliot / Melanson, John Laurence (Cirrus Logi
PATENT AND TRADEMARK OFFICE GRANTED PATENT, May 2008
patno:US7379834
...200 including a sample rate converter (SRC) 201; also...fsi.) c
stream (INPUT DATA) provided...202 to an output digital data st
at...shown in FIG. 4A, a first counter 401a counts periods...MCK :
counter 401b counts periods...
Full text available at patent office. For more in-depth search
[similar results](#)
- ☐ 5. [Systems and methods for clock mode determination utilizing prior](#)
Dewuer, Bruce Eliot / Melanson, John Laurence / Nanda, Ka
UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATE
patno:US7352303
...200 including a sample rate converter (SRC) 201; also...fsi.) c
stream (INPUT DATA) provided...202 to an output digital data st
at...shown in FIG. 4A, a first counter 401a counts periods...MCK :
counter 401b counts periods...
Full text available at patent office. For more in-depth search
[similar results](#)
- ☐ 6. [Systems and methods for clock mode determination utilizing divide](#)
Dewuer, Bruce Eliot / Melanson, John Laurence / Nanda, Ka
UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATE

patno:US7286069

...200 including a sample rate converter (SRC) 201; also...fsi.) c stream (INPUT DATA) provided...202 to an output digital data st at...shown in FIG. 4A, a first counter 401a counts periods...MCK : counter 401b counts periods...

Full text available at patent office. For more in-depth search [similar results](#)

☐ 7. [Systems and methods for clock mode determination utilizing a fixe](#)

Duewer, Bruce Eliot / Melanson, John Laurence (Cirrus Logic) *PATENT AND TRADEMARK OFFICE GRANTED PATENT*, May 2006

patno:US7049988

...200 including a sample rate converter (SRC) 201; also...fsi.) c stream (INPUT DATA) provided...202 to an output digital data st at...shown in FIG. 4A, a first counter 401a counts periods...MCK : counter 401b counts periods...

Full text available at patent office. For more in-depth search [similar results](#)

☐ 8. [Systems and methods for clock mode determination utilizing opera measurement](#)

Duewer, Bruce Eliot / Melanson, John Laurence (Cirrus Logic) *PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Jun 2007

patno:US7236109

...200 including a sample rate converter (SRC) 201; also...fsi.) c stream (INPUT DATA) provided...202 to an output digital data st at...shown in FIG. 4A, a first counter 401a counts periods...MCK : counter 401b counts periods...

Full text available at patent office. For more in-depth search [similar results](#)

☐ 9. [Systems and methods for clock mode determination utilizing explic tables](#)

Duewer, Bruce Eliot / Melanson, John Laurence (Cirrus Logic) *PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Jun 2006

patno:US7057539

...200 including a sample rate converter (SRC) 201; also...fsi.) c stream (INPUT DATA) provided...202 to an output digital data st at...shown in FIG. 4A, a first counter 401a counts periods...MCK : counter 401b counts periods...

Full text available at patent office. For more in-depth search [similar results](#)

☐ [Email](#), [Save](#) or [Export](#) checked results

Sponsored links

[Laurel Digital Meters](#)

OnLine Ordering - Rapid Delivery AC, DC, Temp, Counters 800 576 6308

www.Fiw.com/laurel

[Digital Converter Box](#)

We Accept NTIA Coupons For DTA Boxes. Save \$40! Order Yours Now

www.SolidSignal.com/ConverterBox

[A/D Data Converter](#)

Low Power Consumption High Performance Analog Solutions

www.austriamicrosystems.com

"first counter" "second counter" "sample rate"

[Downloads](#) | [Submit website](#) | [Scirus newsletter](#) | [Help](#) | [Library partners](#) | [Contact us](#)

[About us](#) | [Advisory board](#) | [Privacy policy](#) | [Terms & Conditions](#) | [Newsroom](#)

Powered by FAST © Elsevier 2008



Results of searching in PCT for:
wo/2001/43334: 1 record
Showing record 1 to 1 of 1 :

(Search Summary)

Refine Search wo/2001/43334



Title	Pub. Date	Int. Class	App. Num	Applicant
1. (WO 2001/043334) VOICE GATEWAY WITH DOWNSTREAM VOICE SYNCHRONIZATION	14.06.2001	E05C 1/00	PCT/US2000/033765	BROADCOM CORPORATION

A network gateway is configured to facilitate on line and off line bi-directional communication between a number of near end telephony devices with far end data termination devices via a hybrid fiber coaxial network and a cable modem termination described network gateway combines a QAM receiver, a transmitter, a DOCSIS MAC, a CPU, a voice and audio processor synchronizer, an Ethernet MAC, and a USB controller to provide high performance and robust operation.

Search Summary

WO/2001/43334: 1 occurrence in 1 record.

Search Time: 0.62 seconds.



Publication number: **EP1238489**

Publication date: 2002-09-11

Inventor: RABENKO THEODORE F (US); HARTMAN DAVID (US); THI JAMES C H (US)

Applicant: BROADCOM CORP (US)

Classification

* international: H04B3/23; H04J3/06; H04L12/28; H04L29/06;
H04M7/00; H04N7/24; E05C1/00; H04B3/23; H04J3/06;
H04L12/28; H04L29/06; H04M7/00; H04N7/24;
E05C1/00; (IPC1-7): H04L7/04; H04J3/06; H04M7/00;
H04N7/173

- European: H04L29/06M2S1; H04B3/23; H04B3/23D; H04J3/06B6;
H04J3/06C1; H04L12/28B; H04L29/06G2; H04M7/00B2;
H04N7/24A

Application number: EP20000984300 20001213

Priority number(s): WO2000US33765 20001213; US19990170592P
19991213; US19990170595P 19991213

Also published as



WO0143334 (A3-corr)



WO0143334 (A3)



WO0143334 (A2)



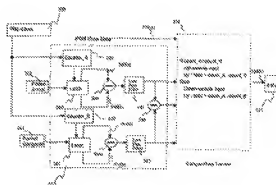
EP1238489 (A0)

[Report a data error here](#)

Abstract not available for EP1238489

Abstract of corresponding document: WO0143334

A network gateway is configured to facilitate on line and off line bi-directional communication between a number of near end data and telephony devices with far end data termination devices via a hybrid fiber coaxial network and a cable modem termination system. The described network gateway combines a QAM receiver, a transmitter, a DOCSIS MAC, a CPU, a voice and audio processor, a voice synchronizer, an Ethernet MAC, and a USB controller to provide high performance and robust operation.



Data supplied from the *esp@cenet* database - Worldwide

1-10 of 16 hits for "first counter" "second counter" "sample rate converter"

☐ Email, Save or Export checked results

Sort

Filter search results by
Content sources
<input type="checkbox"/> Journal sources
<input checked="" type="checkbox"/> Preferred web (16)
▪ Patent Offices (16)
<input type="checkbox"/> Other web
File types
▪ HTML (16)

Refine your search
<ul style="list-style-type: none"> ▪ clock signal ▪ data converter ▪ ratio measurement ▪ clock frequency ▪ frequency measurement ▪ frequency ratio ▪ operating mode ▪ signal generation ▪ mode selection ▪ fractional part
more >

- ☐ 1. **Asynchronous sample rate converter and method**
Yu, Xianggang / Sculley, Terry L. / Chang, Jung-Kuei (Texas Incorporated), *UNITED STATES PATENT AND TRADEMARK OFFICE* 2007
patno:US7262716
...described embodiment of the asynchronous sample rate conversion as shown...1:16 to 16:1. Thus, asynchronous sample rate capability of converting...Greatly simplified, an asynchronous sample rate converter considered to include only...
Full text available at patent office. For more in-depth search [similar results](#)
- ☐ 2. **Asynchronous digital sample rate converter**
Adams, Robert W. / Coln, Michael / Kwan, Tom W. (ANALOG INCORPORATED), *EUROPEAN PATENT APPLICATION*, May 1997
patno:EP774835
...particularly, the invention is a sample rate converter capable of converting a sequence...recorders, and similar devices, need a sample rate converter to audio signals sample...purpose of an asynchronous digital sample rate converter to decouple the sampling rate...
Full text available at patent office. For more in-depth search [similar results](#)
- ☐ 3. **Asynchronous sample rate converter and method**
Yu, Xianggang / Sculley, Terry L. / Chang, Jung-Kuei (TEXAS INCORPORATED), *UNITED STATES PATENT AND TRADEMARK OFFICE PUBLICATION*, Jun 2004
patno:US20040120361
...described embodiment of the asynchronous sample rate conversion as shown...1:16 to 16:1. Thus, asynchronous sample rate capability of converting...Greatly simplified, an asynchronous sample rate converter considered to include only...
Full text available at patent office. For more in-depth search [similar results](#)
- ☐ 4. **ASYNCHRONOUS DIGITAL SAMPLE RATE CONVERTER**
ADAMS, Robert W. / KWAN, Tom W. / COLN, Michael (Analog Incorporated), *EUROPEAN PATENT*, Jul 1995
patno:EP663118
...particularly, the invention is a sample rate converter capable of converting a sequence...recorders, and similar devices, need a sample rate converter to audio signals sample...purpose of an asynchronous digital sample rate converter to decouple the sampling rate...
Full text available at patent office. For more in-depth search [similar results](#)
- ☐ 5. **Asynchronous digital sample rate converter**
Adams, Robert W. / Kwan, Tom W. / Coln, Michael (Analog Incorporated), *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Dec 1995
patno:US5475628
...modifications may be made to optimize the sample rate conversion for various applications. The...described above, in order to design a sample rate converter for digital interpolation...For the preferred embodiment a sample rate conversion accuracy was desired...
Full text available at patent office. For more in-depth search [similar results](#)

similar results

- ☐ 6. Asynchronous sample rate tracker
Savell, Thomas C. / Rossum, David (Creative Technology, Lt
PATENT AND TRADEMARK OFFICE GRANTED PATENT, Nov 2001
patno: US6324235
...PREFERRED EMBODIMENT) 1. Overview of the Sample Rate Cor
invention provides a...would be provided to an asynchronous samp
asynchronous sample rate converter may be used in a digital au
Full text available at patent office. For more in-depth search
similar results
- ☐ 7. Sample rate converter for digital video signals having reduced phase coincidence
Fujita, Tadao (Sony Corporation), *UNITED STATES PATENT AND GRANTED PATENT*, Feb 1994
patno: US5285263
A digital video signal processing device is capable of avoid phase d
case of converting the rate of digital video signals. In case of conve
signal to the second digital video signal, the sync ...
Full text available at patent office. For more in-depth search
similar results
- ☐ 8. Asynchronous sample rate estimation using reciprocal frequency
Savell, Thomas C. (Creative Technology Ltd.), *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Nov 2004
patno: US6819732
...processor operating as an asynchronous sample rate converter
which...electronic devices. An asynchronous sample rate converter
embodiment of the...example, 180° out of phase. In a sample rate
example, if the...
Full text available at patent office. For more in-depth search
similar results
- ☐ 9. Systems and methods for clock mode determination utilizing hysteresis
Duewer, Bruce Eliot / Melanson, John Laurence (Cirrus Logic)
PATENT AND TRADEMARK OFFICE GRANTED PATENT, May 2008
patno: US7379834
...processing system 200 including a sample rate converter (SRC
describing...embodiment shown in FIG. 4A, a first counter 401a c
signal and a second counter 401b counts periods of the FIXCLK...
Full text available at patent office. For more in-depth search
similar results
- ☐ 10. Systems and methods for clock mode determination utilizing priority
Duewer, Bruce Eliot / Melanson, John Laurence / Nanda, Ka
UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT
patno: US7352303
...processing system 200 including a sample rate converter (SRC
describing...embodiment shown in FIG. 4A, a first counter 401a c
signal and a second counter 401b counts periods of the FIXCLK...
Full text available at patent office. For more in-depth search
similar results

☐ [Email](#), [Save](#) or [Export](#) checked results

Sponsored links

LED Rate / TAKT Timers

Count By 1, 3, 10, 100 / Any Number Industry / Exhibits 800-367-6056

www.electronicdisplays.com

[Travel Converter Adapter](#)

Solving international electricity since 1995. 12% off sale.

www.WalkaboutTravelGear.com

[Sample Rate Conversion](#)

Automatically design multistage FIR filters for sample rate conversion

www.mds.com

[Previous](#)

[1](#) [2](#)

"first counter" "second counter" "sample rate

[Downloads](#) | [Submit website](#) | [Scirus newsletter](#) | [Help](#) | [Library partners](#) | [Contact us](#)

[About us](#) | [Advisory board](#) | [Privacy policy](#) | [Terms & Conditions](#) | [Newsroom](#)

Powered by FAST © Elsevier 2008

11-16 of 16 hits for "first counter" "second counter" "sample rate converter"

☐ Email, Save or Export checked results

Sor

Filter search results by

Content sources

☐ Journal sources

☒ Preferred web (16)

▪ Patent Offices (16)

☐ Other web

File types

▪ HTML (16)

Refine your search

- clock signal
- data converter
- ratio measurement
- clock frequency
- frequency measurement
- frequency ratio
- operating mode
- signal generation
- mode selection
- fractional part

[more >](#)

- ☐ 11. **VOICE GATEWAY WITH DOWNSTREAM VOICE SYNCHRONIZATION**
RABENKO, Theodore F. / HARTMAN, David / THI, James, C., Corporation), *EUROPEAN PATENT*, Sep 2002
patno:EP1238489
...text> discloses the use of a sample rate converter (SRC) recei stream at a first...stream at a second sample rate. The sampling re converter is controlled depending on the fill state of the buffer...
Full text available at patent office. For more in-depth search [similar results](#)
- ☐ 12. **Systems and methods for clock mode determination utilizing divide**
Dewer, Bruce Eliot / Melanson, John Laurence / Nanda, Ka *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATE*
patno:US7286069
...processing system 200 including a sample rate converter (SRC) describing...embodiment shown in FIG. 4A, a first counter 401a c signal and a second counter 401b counts periods of the FIXCLK...
Full text available at patent office. For more in-depth search [similar results](#)
- ☐ 13. **Systems and methods for clock mode determination utilizing a fixe**
Dewer, Bruce Eliot / Melanson, John Laurence (Cirrus Logi *PATENT AND TRADEMARK OFFICE GRANTED PATENT*, May 2006
patno:US7049988
...processing system 200 including a sample rate converter (SRC) describing...embodiment shown in FIG. 4A, a first counter 401a c signal and a second counter 401b counts periods of the FIXCLK...
Full text available at patent office. For more in-depth search [similar results](#)
- ☐ 14. **Systems and methods for clock mode determination utilizing opera measurement**
Dewer, Bruce Eliot / Melanson, John Laurence (Cirrus Logi *PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Jun 2007
patno:US7236109
...processing system 200 including a sample rate converter (SRC) describing...embodiment shown in FIG. 4A, a first counter 401a c signal and a second counter 401b counts periods of the FIXCLK...
Full text available at patent office. For more in-depth search [similar results](#)
- ☐ 15. **Systems and methods for clock mode determination utilizing explic tables**
Dewer, Bruce Eliot / Melanson, John Laurence (Cirrus Logi *PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Jun 2006
patno:US7057539
...processing system 200 including a sample rate converter (SRC) describing...embodiment shown in FIG. 4A, a first counter 401a c signal and a second counter 401b counts periods of the FIXCLK...
Full text available at patent office. For more in-depth search [similar results](#)
- ☐ 16. **CIRCUITS, SYSTEM, AND METHODS FOR PROCESSING MULTIPLE I**
NORTH, Gregory, Allen / GEPHARDT, Douglas / BARNETTE, J

James D. / HABAN, Scott, Thomas / DAVID, Thomas, Sarosh
Christopher (CIRRUS LOGIC, INC.), *PATENT COOPERATION TR*
1998

patno:WO9835301

An audio information processing subsystem (200) is disclosed whic
processor (100) for simultaneously processing multiple streams of
subsystem (200) also includes a program memory (202) coupled to

Full text available at patent office. For more in-depth search
[similar results](#)



[Email](#), [Save](#) or [Export](#) checked results

Sponsored links

[2nd First](#)

2nd First Online. Shop Target.com.

www.Target.com

[Previous](#)

[1 2](#)

"first counter" "second counter" "sample rate

[Search](#)

[Downloads](#) | [Submit website](#) | [Scirus newsletter](#) | [Help](#) | [Library partners](#) | [Contact us](#)

[About us](#) | [Advisory board](#) | [Privacy policy](#) | [Terms & Conditions](#) | [Newsroom](#)

Powered by FAST © Elsevier 2008



Home IP Services PATENTSCOPE Patent Search

Results of searching in PCT for:

WO/1998/35301: 1 record

Showing record 1 to 1 of 1 :

(Search Summary)

Refine Search WO/1998/35301

RSS

Title	Pub. Date	Int. Class	App. Num	App
1. (WO/1998/035301) CIRCUITS, SYSTEM, AND METHODS FOR PROCESSING MULTIPLE DATA STREAMS	13.08.1998	G10H 7/00	PCT/US1998/002319	CIRF INC.

An audio information processing subsystem (200) is disclosed which includes a stream processor (100) for simultaneously multiple streams of audio data. Processing subsystem (200) also includes a program memory (202) coupled to stream processor (100) for storing instructions for controlling processing system (200) and a data memory (203/204) also coupled to stream processor (100). Additionally, a direct memory access circuitry (208) is provided for controlling direct memory accesses to a selected one of memory (202) and data memory (203/204).

Search Summary

WO/1998/35301: 1 occurrence in 1 record.

Search Time: 0.13 seconds.



Correspondence Address for 10/805591

Customer Number	Contact Information	Address
42671 Delivery Mode: <u>Paper</u>	Telephone: (512)306-9200 Fax: (512) 30-6 99 E-Mail: <u>MBERRIER@TEXASIP.COM</u>	LAW OFFICES OF MARK L. BERRIER 3811 BEE CAVES ROAD SUITE 204 AUSTIN TX 78746

[Appln Info](#)[Contents](#)[Petition Info](#)[Atty/Agent Info](#)[Continuity/Reexam](#)[Foreign C](#)

Search Another: Application #

or Patent#

PCT / /

or PG PUBS #

Attorney Docket #

Bar Code #

To go back, right click here and select Back. To go forward, right click here and select Forward. To refresh, right click here and select Refresh.

[Back to QASIS](#) | [Home page](#)

Continuity/Reexam Information for 10/805591

Parent Data

10805591, filed 03/19/2004

Claims Priority from Provisional Application 60469725, filed 05/12/2003

Claims Priority from Provisional Application 60456414, filed 03/21/2003

Claims Priority from Provisional Application 60456430, filed 03/21/2003

Claims Priority from Provisional Application 60456429, filed 03/21/2003

Claims Priority from Provisional Application 60456421, filed 03/21/2003

Child Data

No Child Data

[Appln Info](#)[Contents](#)[Petition Info](#)[Atty/Agent Info](#)[Continuity/Reexam](#)[Foreign Data](#)

Search Another: Application #

or Patent#

PCT / /

or PG PUBS #

Attorney Docket #

Bar Code #

To go back, right click here and select Back. To go forward, right click here and select Forward. To refresh, right click here and select Refresh.

Back to [OASIS](#) Home page

Foreign Information for 10/805591

No Foreign Data

Appln Info	Contents	Petition Info	Atty/Agent Info	Continuity/Reexam	Foreign Data <input type="checkbox"/>
------------	----------	---------------	-----------------	-------------------	---------------------------------------

Search Another: Application #

or Patent#

 PCT / /

or PG PUBS #

 Attorney Docket # Bar Code #

To go back, right click here and select Back. To go forward, right click here and select Forward. To refresh, right click here and select Refresh.

Back to [OASIS](#) | [Home page](#)

Application Number Information

Application Number: 10/805591

Assignments

Filing or 371(c) Date: 03/19/2004 eDan

Effective Date: 03/19/2004

Application Received: 03/22/2004

Patent Number:

Issue Date: 00/00/0000

Date of Abandonment: 00/00/0000

Attorney Docket Number: D2A1240-1

Status: 80 /RESPONSE AFTER FINAL ACTION FORWARDED TO EXAMINER

Confirmation Number: 9251

Examiner Number: 80488 / TORRES, JUAN

Group Art Unit: 2611

IEW Madras

Class/Subclass:

375/355.000

Lost Case: NO

Waiting for Response

Interference Number:

Desc.

Unmatched Petition: NO

Amndt.aft final

L&R Code: Secrecy Code:1

Third Level Review: NO

Secrecy Order: NO

Status Date: 08/22/2008

Oral Hearing: NO

Title of Invention: SYSTEMS AND METHODS FOR SAMPLE RATE CONVERSION USING MULTIPLE RATE ESTIMATE COUNTERS

Bar Code	PALM Location	Location Date	Charge to Loc	Charge to Name	Employee Name	Location
----------	---------------	---------------	---------------	----------------	---------------	----------

Appln
Info

Contents

Petition Info

Atty/Agent Info

Continuity/Reexam

Foreign Data

Search Another: Application #

Search

or Patent#

Search

PCT / /

Search

or PG PUBS #

Search

Attorney Docket #

Search

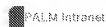
Bar Code #

Search

To go back, right click here and select Back. To go forward, right click here and select Forward. To refresh, right click here and select Refresh.

Back to [OASIS](#) | Home page

http://EXPWEB1:8001/cgi-bin/expo/GenInfo/snquery.pl?APPL_ID=10805591



Application
Number

Submit

IDS Flag Clearance for Application 10805591

**IDS
Information**

Content	Mailroom Date	Entry Number	IDS Review	Last Modified	Reviewer
Update					

Inventor Information for 10/805591

Inventor Name	City	State/Country
CHIENG, DANIEL L. W.	AUSTIN	TEXAS
ANDERSEN, JACK B.	AUSTIN	TEXAS
HAND, LARRY E.	MERIDIAN	MISSISSIPPI

[Appln Info](#)[Contents](#)[Petition Info](#)[Atty/Agent Info](#)[Continuity/Reexam](#)[Foreign E](#)

Search Another: Application #

or Patent#

PCT / /

or PG PUBS #

Attorney Docket #

Bar Code #

To go back, right click here and select Back. To go forward, right click here and select Forward. To refresh, right click here and select Refresh.

Back to [OASIS](#) | [Home page](#)

Day : Sunday
Date: 8/24/2008


PALM INTRANET

Time: 14:04:44

Inventor Name Search Result

Your Search was:

Last Name = CHIENG

First Name = DANIEL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
60755560	Not Issued	159	12/30/2005	Systems amd methods for power supply tracking	CHIENG, DANIEL
60771146	Not Issued	159	02/07/2006	PWM feedback/feed-forward protection	CHIENG, DANIEL
60771147	Not Issued	159	02/07/2006	Full bridge PWM feedback analog input filter component mismatch correction	CHIENG, DANIEL
60771212	Not Issued	159	02/07/2006	Power supply feed forward analog input filter component mismatch correction	CHIENG, DANIEL
60969608	Not Issued	20	09/01/2007	Systems and Methods for HDA Codec with Integrated Class-D PWM Controller to Handle HDA Volume Control	CHIENG, DANIEL L.
60969609	Not Issued	20	09/01/2007	HDA Communication between the Application and the DSP in a HDA Audio Codec for Local Intelligent Processing	CHIENG, DANIEL L.
60969610	Not Issued	20	09/01/2007	DSP Responses to Unsupported Verbs in a HDA Audio Codec to Extend Existing Features or to Implement New Features	CHIENG, DANIEL L.
60969611	Not Issued	20	09/01/2007	Booting over the High Definition Audio Bus	CHIENG, DANIEL L.
60969612	Not Issued	20	09/01/2007	Using DSP to Override Verb Responses to Reconfigure the HDA Audio Codec	CHIENG, DANIEL L.
60969613	Not Issued	20	09/01/2007	Standalone HDA Audio Codec With Integrated Class-D PWM Controller/Amplifier That Has The Ability To Shadow Other HDA Codecs	CHIENG, DANIEL L.
10805590	Not	41	03/19/2004	Phase alignment of audio output	CHIENG, DANIEL

	Issued			data in a multi-channel configuration	L. W.
10805591	Not Issued	80	03/19/2004	Systems and methods for sample rate conversion using multiple rate estimate counters	CHIENG, DANIEL L. W.
10805593	Not Issued	41	03/19/2004	Multi-chip PWM synchronization and communication	CHIENG, DANIEL L. W.
10805596	Not Issued	61	03/19/2004	SRC with multiple sets of filter coefficients in memory and a high order coefficient interpolator	CHIENG, DANIEL L. W.
11324132	7286009	150	12/30/2005	DIGITAL PWM AMPLIFIER WITH SIMULATION-BASED FEEDBACK	CHIENG, DANIEL L. W.
11340139	7286010	150	01/26/2006	SYSTEMS AND METHODS FOR OVER-CURRENT PROTECTION	CHIENG, DANIEL L. W.
11672191	Not Issued	41	02/07/2007	PWM Feedback/Feed-forward Protection	CHIENG, DANIEL L. W.
11672321	Not Issued	93	02/07/2007	POWER SUPPLY FEED FORWARD ANALOG INPUT FILTER COMPONENT MISMATCH CORRECTION	CHIENG, DANIEL L. W.
60615674	Not Issued	159	10/04/2004	Simulation-based feedback	CHIENG, DANIEL L. W.
10805569	7167112	150	03/20/2004	SYSTEMS AND METHODS FOR IMPLEMENTING A SAMPLE RATE CONVERTER USING HARDWARE AND SOFTWARE TO MAXIMIZE SPEED AND FLEXIBILITY	CHIENG, DANIEL L.W.
10805574	Not Issued	41	03/20/2004	Streaming multi-channel audio as packetized data or parallel data with a separate input frame sync	CHIENG, DANIEL L.W.
11672331	Not Issued	100	02/07/2007	SYSTEMS AND METHODS FOR CORRECTING ERRORS RESULTING FROM COMPONENT MISMATCH IN A FEEDBACK PATH	CHIENG, DANIEL L.W.
11782702	Not Issued	30	07/25/2007	Low-Noise, Low-Distortion Digital PWM Amplifier	CHIENG, DANIEL L.W.
11782708	Not Issued	30	07/25/2007	Digital PWM Amplifier Having a Low Delay Corrector	CHIENG, DANIEL L.W.
60456429	Not Issued	159	03/21/2003	High efficiency, high-performance sample rate converter	CHIENG, DANIEL L.W.
60469725	Not	159	05/12/2003	SRC with dual input rate estimator	CHIENG, DANIEL

	Issued			counters for automatic second sample rate detection	L.W.
60469734	Not Issued	159	05/12/2003	Multi-chip PWM synchronization and communication	CHIENG, DANIEL L.W.
60469735	Not Issued	159	05/12/2003	SRC with multiple sets of filter coefficients in memory and a high order coefficient interpolator	CHIENG, DANIEL L.W.
60469761	Not Issued	159	05/12/2003	Systems and methods for implementing a sample rate converter using hardware and software to maximize speed and flexibility	CHIENG, DANIEL L.W.
60469774	Not Issued	159	05/12/2003	Phase alignment of output audio data in a multi-SRC configuration	CHIENG, DANIEL L.W.
60469804	Not Issued	159	05/12/2003	Streaming multi-channel audio as packetized data or parallel data with a separate input frame sync	CHIENG, DANIEL L.W.

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name
 CHIENG

First Name
 DANIEL

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Sunday
Date: 8/24/2008

Time: 18:53:33

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = ANDERSEN

First Name = JACK

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10329852	6741123	150	12/26/2002	DELTA-SIGMA AMPLIFIERS WITH OUTPUT STAGE SUPPLY VOLTAGE VARIATION COMPENSATION AND METHODS AND DIGITAL AMPLIFIER SYSTEMS USING THE SAME	ANDERSEN, JACK
60755560	Not Issued	159	12/30/2005	Systems and methods for power supply tracking	ANDERSEN, JACK
60761614	Not Issued	159	01/24/2006	Systems and methods for reducing distortion and avoiding AM radio interference in a class D amplifier by adding a tone at half the switching frequency	ANDERSEN, JACK
60763614	Not Issued	159	01/31/2006	Asymmetric pulse width in 2-level full-bridge PWM output stages	ANDERSEN, JACK
60771146	Not Issued	159	02/07/2006	PWM feedback/feed-forward protection	ANDERSEN, JACK
60771147	Not Issued	159	02/07/2006	Full bridge PWM feedback analog input filter component mismatch correction	ANDERSEN, JACK
60771212	Not Issued	159	02/07/2006	Power supply feed forward analog input filter component mismatch correction	ANDERSEN, JACK
10314804	6762704	150	12/09/2002	MODULATION OF A DIGITAL INPUT SIGNAL USING MULTIPLE DIGITAL SIGNAL MODULATORS	ANDERSEN, JACK B.
10325145	6693571	150	12/20/2002	MODULATION OF A DIGITAL INPUT SIGNAL USING A DIGITAL SIGNAL MODULATOR AND SIGNAL SPLITTING	ANDERSEN, JACK B.

10328281	6925115	150	12/23/2002	APPARATUS AND METHOD FOR SAFELY HANDLING ASYNCHRONOUS SHUTDOWN OF PULSEWIDTH MODULATED OUTPUT	ANDERSEN, JACK B.
10805569	7167112	150	03/20/2004	SYSTEMS AND METHODS FOR IMPLEMENTING A SAMPLE RATE CONVERTER USING HARDWARE AND SOFTWARE TO MAXIMIZE SPEED AND FLEXIBILITY	ANDERSEN, JACK B.
10805574	Not Issued	41	03/20/2004	Streaming multi-channel audio as packetized data or parallel data with a separate input frame sync	ANDERSEN, JACK B.
10805588	Not Issued	71	03/19/2004	Clip detection in PWM amplifier	ANDERSEN, JACK B.
10805589	Not Issued	93	03/19/2004	OUTPUT STAGE SYNCHRONIZATION	ANDERSEN, JACK B.
10805590	Not Issued	41	03/19/2004	Phase alignment of audio output data in a multi-channel configuration	ANDERSEN, JACK B.
10805591	Not Issued	80	03/19/2004	Systems and methods for sample rate conversion using multiple rate estimate counters	ANDERSEN, JACK B.
10805592	7078963	150	03/19/2004	INTEGRATED PULSHI MODE WITH SHUTDOWN	ANDERSEN, JACK B.
10805593	Not Issued	41	03/19/2004	Multi-chip PWM synchronization and communication	ANDERSEN, JACK B.
10805594	Not Issued	61	03/19/2004	Systems and methods for protection of audio amplifier circuits	ANDERSEN, JACK B.
10805596	Not Issued	61	03/19/2004	SRC with multiple sets of filter coefficients in memory and a high order coefficient interpolator	ANDERSEN, JACK B.
10805741	7023268	150	03/22/2004	SYSTEMS AND METHODS FOR AUTOMATICALLY ADJUSTING CHANNEL TIMING	ANDERSEN, JACK B.
10843851	7061312	150	05/12/2004	SYSTEMS AND METHODS FOR PROVIDING MULTI CHANNEL PULSE WIDTH MODULATED AUDIO WITH STAGGERED OUTPUTS	ANDERSEN, JACK B.
10843852	Not Issued	71	05/12/2004	Systems and methods for switching and mixing signals in a multi-channel amplifier	ANDERSEN, JACK B.

11324132	7286009	150	12/30/2005	DIGITAL PWM AMPLIFIER WITH SIMULATION-BASED FEEDBACK	ANDERSEN, JACK B.
11333709	7315264	150	01/17/2006	SYSTEMS AND METHODS FOR CONTROLLING TRANSIENT RESPONSE IN THE OUTPUT OF A NOISE SHAPER	ANDERSEN, JACK B.
11340139	7286010	150	01/26/2006	SYSTEMS AND METHODS FOR OVER-CURRENT PROTECTION	ANDERSEN, JACK B.
11626569	Not Issued	41	01/24/2007	Systems and Methods for Improving Performance in a Digital Amplifier by Adding an Ultrasonic Signal to an Input Audio Signal	ANDERSEN, JACK B.
11669643	Not Issued	95	01/31/2007	SYSTEMS AND METHODS FOR PULSE WIDTH MODULATING ASYMMETRIC SIGNAL LEVELS	ANDERSEN, JACK B.
11672191	Not Issued	41	02/07/2007	PWM Feedback/Feed-forward Protection	ANDERSEN, JACK B.
11672321	Not Issued	93	02/07/2007	POWER SUPPLY FEED FORWARD ANALOG INPUT FILTER COMPONENT MISMATCH CORRECTION	ANDERSEN, JACK B.
11672331	Not Issued	100	02/07/2007	SYSTEMS AND METHODS FOR CORRECTING ERRORS RESULTING FROM COMPONENT MISMATCH IN A FEEDBACK PATH	ANDERSEN, JACK B.
11782702	Not Issued	30	07/25/2007	Low-Noise, Low-Distortion Digital PWM Amplifier	ANDERSEN, JACK B.
11782708	Not Issued	30	07/25/2007	Digital PWM Amplifier Having a Low Delay Corrector	ANDERSEN, JACK B.
60456414	Not Issued	159	03/21/2003	Adaptive anti-clipping protection	ANDERSEN, JACK B.
60456421	Not Issued	159	03/21/2003	Output device switch timing correction	ANDERSEN, JACK B.
60456427	Not Issued	159	03/21/2003	Intelligent over-current, over-load protection	ANDERSEN, JACK B.
60456429	Not Issued	159	03/21/2003	High efficiency, high-performance sample rate converter	ANDERSEN, JACK B.
60469640	Not	159	05/12/2003	Multi channel PWM with	ANDERSEN, JACK

	Issued			staggered outputs	B.
60469725	Not Issued	159	05/12/2003	SRC with dual input rate estimator counters for automatic second sample rate detection	ANDERSEN, JACK B.
60469734	Not Issued	159	05/12/2003	Multi-chip PWM synchronization and communication	ANDERSEN, JACK B.
60469735	Not Issued	159	05/12/2003	SRC with multiple sets of filter coefficients in memory and a high order coefficient interpolator	ANDERSEN, JACK B.
60469760	Not Issued	159	05/12/2003	Integrated pulshi mode with shutdown	ANDERSEN, JACK B.
60469761	Not Issued	159	05/12/2003	Systems and methods for implementing a sample rate converter using hardware and software to maximize speed and flexibility	ANDERSEN, JACK B.
60469770	Not Issued	159	05/12/2003	Clip detection in PWM amplifier	ANDERSEN, JACK B.
60469774	Not Issued	159	05/12/2003	Phase alignment of output audio data in a multi-SRC configuration	ANDERSEN, JACK B.
60469776	Not Issued	159	05/12/2003	Marco PWM protection logic	ANDERSEN, JACK B.
60469787	Not Issued	159	05/12/2003	PWM output stage synchronization	ANDERSEN, JACK B.
60469804	Not Issued	159	05/12/2003	Streaming multi-channel audio as packetized data or parallel data with a separate input frame sync	ANDERSEN, JACK B.
60615674	Not Issued	159	10/04/2004	Simulation-based feedback	ANDERSEN, JACK B.
60878708	Not Issued	159	01/05/2007	Edible puzzle candy bar	ANDERSEN, JACKSON G.

[Search and Display More Records.](#)

Search Another: Inventor **ANDERSEN**

First Name
JACK

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Sunday
Date: 8/24/2008

Time: 18:54:54

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = HAND

First Name = LARRY

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10805569	7167112	150	03/20/2004	SYSTEMS AND METHODS FOR IMPLEMENTING A SAMPLE RATE CONVERTER USING HARDWARE AND SOFTWARE TO MAXIMIZE SPEED AND FLEXIBILITY	HAND, LARRY E.
10805588	Not Issued	71	03/19/2004	Clip detection in PWM amplifier	HAND, LARRY E.
10805590	Not Issued	41	03/19/2004	Phase alignment of audio output data in a multi-channel configuration	HAND, LARRY E.
10805591	Not Issued	80	03/19/2004	Systems and methods for sample rate conversion using multiple rate estimate counters	HAND, LARRY E.
10805596	Not Issued	61	03/19/2004	SRC with multiple sets of filter coefficients in memory and a high order coefficient interpolator	HAND, LARRY E.
10843852	Not Issued	71	05/12/2004	Systems and methods for switching and mixing signals in a multi-channel amplifier	HAND, LARRY E.
11211765	7259618	150	08/25/2005	SYSTEMS AND METHODS FOR LOAD DETECTION AND CORRECTION IN A DIGITAL AMPLIFIER	HAND, LARRY E.
11324132	7286009	150	12/30/2005	DIGITAL PWM AMPLIFIER WITH SIMULATION-BASED FEEDBACK	HAND, LARRY E.
11340139	7286010	150	01/26/2006	SYSTEMS AND METHODS FOR OVER-CURRENT PROTECTION	HAND, LARRY E.
11782702	Not Issued	30	07/25/2007	Low-Noise, Low-Distortion Digital PWM Amplifier	HAND, LARRY E.

11782708	Not Issued	30	07/25/2007	Digital PWM Amplifier Having a Low Delay Corrector	HAND, LARRY E.
60456414	Not Issued	159	03/21/2003	Adaptive anti-clipping protection	HAND, LARRY E.
60456422	Not Issued	159	03/21/2003	Output filter, phase/timing correction	HAND, LARRY E.
60456427	Not Issued	159	03/21/2003	Intelligent over-current, over-load protection	HAND, LARRY E.
60456428	Not Issued	159	03/21/2003	Output filter speaker/load compensation	HAND, LARRY E.
60456429	Not Issued	159	03/21/2003	High efficiency, high-performance sample rate converter	HAND, LARRY E.
60456430	Not Issued	159	03/21/2003	Frequency response correction	HAND, LARRY E.
60469725	Not Issued	159	05/12/2003	SRC with dual input rate estimator counters for automatic second sample rate detection	HAND, LARRY E.
60469735	Not Issued	159	05/12/2003	SRC with multiple sets of filter coefficients in memory and a high order coefficient interpolator	HAND, LARRY E.
60469761	Not Issued	159	05/12/2003	Systems and methods for implementing a sample rate converter using hardware and software to maximize speed and flexibility	HAND, LARRY E.
60469762	Not Issued	159	05/12/2003	PWM software protection schemes	HAND, LARRY E.
60469770	Not Issued	159	05/12/2003	Clip detection in PWM amplifier	HAND, LARRY E.
60469774	Not Issued	159	05/12/2003	Phase alignment of output audio data in a multi-SRC configuration	HAND, LARRY E.
60969608	Not Issued	20	09/01/2007	Systems and Methods for HDA Codec with Integrated Class-D PWM Controller to Handle HDA Volume Control	HAND, LARRY E.
60969609	Not Issued	20	09/01/2007	HDA Communication between the Application and the DSP in a HDA Audio Codec for Local Intelligent Processing	HAND, LARRY E.
60969614	Not Issued	20	09/01/2007	Intelligent "Mode Control" Using Plug-In Personalities	HAND, LARRY E.
60969615	Not	20	09/01/2007	Scalable Output Configurations	HAND, LARRY E.

	Issued			for Smart Amplifiers	
60988365	Not Issued	20	11/15/2007	Switching Amplifier Optimized for Minimal and Stable Output Semiconductor Dead Times	HAND, LARRY E.
06643315	4600891	150	08/21/1984	DIGITAL AUDIO AMPLIFIER HAVING A HIGH POWER OUTPUT LEVEL AND LOW DISTORTION	HAND, LARRY E.
06643316	4611300	150	08/21/1984	DIGITAL DELAY LINE	HAND, LARRY E.
06874379	4724396	150	06/13/1986	DIGITAL AUDIO AMPLIFIER	HAND, LARRY E.
07211217	RE33333	150	03/11/1988	DIGITAL AUDIO AMPLIFIER HAVING A HIGH POWER OUTPUT LEVEL AND LOW DISTORTION	HAND, LARRY E.
07422518	4992751	150	10/17/1989	AUDIO AMPLIFIER WITH PHASE MODULATED PULSE WIDTH MODULATION	HAND, LARRY E.
09365019	6239655	150	08/02/1999	MICROPHONE AMPLIFIER WITH DIGITAL GAIN CONTROL	HAND, LARRY EUGENE
60128305	Not Issued	159	04/08/1999	MICROPHONE AMPLIFIER WITH DIGITAL CONTROL	HAND, LARRY EUGENE
60380029	Not Issued	159	05/02/2002	System and process for detection of weak or non-functioning cylinders in engines	HANDLER, LARRY
10108971	6802221	150	03/27/2002	SYSTEM AND METHOD FOR CONDITIONED-BASED MONITORING OF A BEARING ASSEMBLY	HANDLER, LARRY R.
10158750	Not Issued	161	05/30/2002	System and method for conditioned based monitoring using acoustic diagnosis	HANDLER, LARRY R.
10159512	6985803	150	05/30/2002	SYSTEM AND METHOD FOR MONITORING THE CONDITION OF A VEHICLE	HANDLER, LARRY R.
60279650	Not Issued	159	03/29/2001	Automatic analysis and detection of faults	HANDLER, LARRY R.
60294330	Not Issued	159	05/30/2001	Automated smart underpass system for predictively monitoring and early determining the condition of land-based transportation assets	HANDLER, LARRY R.
60294354	Not	159	05/30/2001	Condition based monitoring for	HANDLER, LARRY

	Issued		land-based equipment	R.
--	--------	--	----------------------	----

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	
	HAND	LARRY	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)